

Docket No.: 211200US2



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Toshitada SAITO

SERIAL NO.: 09/900,945

GAU: 2133

FILED: July 10, 2001

EXAMINER: TRIMMINGS, J. P.

FOR: ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED CIRCUIT INCLUDING
PROCESSOR CIRCUIT AND ITS PERIPHERAL CIRCUITS

LETTER SUBMITTING REPLACEMENT DRAWING SHEETS

RECEIVED

COMMISSIONER FOR PATENTS
Alexandria, VA 22313

JUN 15 2004

Technology Center 2100

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

☒ 1 Replacement Drawing Sheets ☐ _____ New Drawing Sheets

- ☒ Official Action dated December 8, 2003
☐ Notice of Allowance/Issue Fee dated _____
☐ Other dated _____

The changes and/or modifications made include the following:

The attached sheet of drawings includes changes to Fig. 6. This sheet, which includes Fig. 6, replaces the original sheet including Fig. 6.

Respectfully submitted,

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 05/2003)

I:\ATTY\ZS\21\S\2112\211200US\211200 LETTER FOR DRAW.DOC